



PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Rambaldi et al.

Atty Dkt No.: SGSTP009D1/97-B-068D1

Application No.: 10/613,830

Examiner: Vu, Ngoc Yen T

Filed: July 3, 2003

Group: 2612

Title: PIXEL CORRECTION SYSTEM AND  
METHOD FOR CMOS IMAGERS

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on January 25, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: \_\_\_\_\_

Joyce L. Ferreira

**INFORMATION DISCLOSURE STATEMENT  
37 CFR §§1.56 AND 1.97(b)**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
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Dear Sir:

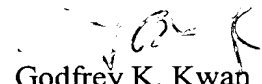
The references listed in the attached PTO Form 1449 may be material to examination of the above-identified patent application. Applicants submit the list of these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application. The above-identified application is a divisional of prior application U.S. Patent Application No. 08/964,763. This prior application is being relied upon for an earlier filing date under 35 U.S.C. § 120. Because the listed references were either cited by the PTO, or submitted to the PTO in the prior application, under 37 CFR § 1.98(d) Applicants submit that copies need not be provided.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

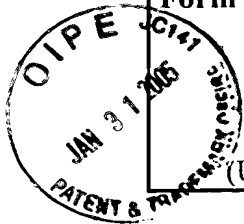
This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first

Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. SGSGP009D1).

Respectfully submitted,  
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<b>Form 1449 (Modified)</b>  <b>Information Disclosure Statement By Applicant</b>  (Use Several Sheets if Necessary)	Atty Docket No.      Application No.: SGSTP009D1/97-B-068D1    10/613,830 Applicant: Rambaldi et al. Filing Date                      Group July 3, 2003                      2612
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**U.S. Patent Documents**

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
	A	4,399,464	Aug. 1983	Hix			
	B	4,541,116	Sep. 1985	Lougheed			
	C	4,567,525	Jan. 1986	Endo			
	D	4,701,784	Oct. 1987	Matsuoka			
	E	4,805,023	Feb. 1989	Younse			
	F	4,858,013	Aug. 1989	Matsuda			
	G	5,047,861	Sep. 1991	Houchin			
	H	5,101,271	Mar. 1992	Andrews			
	I	5,153,421	Oct., 1992	Tandon			
	J	5,168,379	Dec. 1992	Mori			
	K	5,204,948	Apr. 1993	Kato			
	L	5,416,516	May 1995	Kameyama			
	M	5,451,768	Sep. 1995	Hosier			
	N	5,532,484	Jul. 1996	Sweetser et al.			
	O	5,796,430	Aug. 1998	Katoh			
	P	5,886,353	Mar. 1999	Spivey			
	Q	6,057,539	May 2000	May., 2000			

**Foreign Patent or Published Foreign Patent Application**

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	R	0350328	Jan., 1990	EPO				

**Other Documents**

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	S	Eiji Oba, et al., "A 1/4 Inch 330k Square Pixel Progressive Scan CMOS Active Pixel Image Sensor," Feb. 7, 1997, Digest of Technical Papers, pp. 180-181.
	T	McGrath, R. Daniel, et al., "Current-Mediated, Current-Reset 768.times.512 Active Pixel Sensor Array," Feb. 7, 1997, Digest of Technical Papers, pp. 182-183.
	U	Eric R. Fossum, "CMOS Image Sensors: Electronic Camera On a Chip," Dec. 10-13, 1995, International Electron Devices Meeting Technical Digest, pp. 17-25.
	V	Goodenough, Frank, "Get Ready for TV Cameras on a Chip," Electronic Design Report, vol. 44, No. 4, 1996, Cleveland, OH.
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.